

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 243154US2		SERIAL NO. New Application	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Hidehiro TAKATA			
				FILING DATE Herewith		GROUP	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
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	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
	AO						
	AP						
	AQ						
	AR						
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
<i>aw</i>	AW	Brian Curran, et al., "A 1.1 GHz First 64b Generation Z900 Microprocessor" 2001 IEEE INTERNATIONAL SOLID-STATE CIRCUITS CONFERENCE, ISSCC 2001/SESSION 15/ MICROPROCESSORS/ 15.5, pgs 238-239 & 454					
<i>aw</i>	AX	Hidehiro TAKATA, et al., "Physical Design Methodology for On-Chip 64-Mb DRAM MPEG-2 Encoding with a Multimedia Processor", IEICE TRANSACTIONS ON ELECTRONICS, Vol. E85-C, No. 2, February 2002, pgs. 368-374					
<i>aw</i>	AY	Kouichi Yamguchi, et al., "2.5 GHz 4-phase Clock Generator with Scalable and No Feedback Loop Architecture" 2001 IEEE INTERNATIONAL SOLID-STATE CIRCUITS CONFERENCE, ISSCC 2001/SESSION 25/CLOCK GENERATION AND DISTRIBUTION/25.4, pgs. 398-399 & 326-327					
<i>aw</i>	AZ	Thucydides Xanthopoulos, et al., "The Design and Analysis of the Clock Distribution Network for a 1.2 GHz Alpha Microprocessor" 2001 IEEE INTERNATIONAL SOLID-STATE CIRCUITS CONFERENCE, ISSCC 2001/SESSION 25/CLOCK GENERATION AND DISTRIBUTION/25.6, pgs.402-403 & 330-331				<input type="checkbox"/> Additional References sheet(s) attached	
Examiner <i>A. Wang</i>					Date Considered <i>12-7-05</i>		
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							